

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

JIN-SUNG KIM *et al.*

Serial No.: *to be assigned*

Examiner: *to be assigned*

Filed: 7 July 2003

Art Unit: *to be assigned*

For: METHOD OF DRIVING 3-ELECTRODE PLASMA DISPLAY APPARATUS TO
MINIMIZE ADDRESSING POWER

INFORMATION DISCLOSURE STATEMENT

Mail Stop Patent Application

Commissioner for Patents

P.O.Box 1450

Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §1.56, and §§1.97 and 1.98 as amended, Applicant cites, describes and provides a copy of the following art reference:

U.S. Patent No. 5,541,618 to Shinoda, entitled *METHOD AND A CIRCUIT FOR GRADATIONALLY DRIVING A FLAT DISPLAY DEVICE*, issued on July 30, 1996. Shinoda '618 pertains to a method and circuit of driving a flat display panel formed of a plurality of cells, each having a memory functionse, where the cells are formed at cross points of a plurality of X-electrodes and a plurality of Y-electrode orthogonal to the X-electrodes, a period of a frame for displaying a single picture is divided into a plurality of sequential subframes.

The citation of the foregoing reference is not intended to constitute an assertion that other or more relevant art does not exist. Accordingly, the Examiner is requested to make a wide-ranging and thorough search of the relevant art.

No fee is incurred by this Statement.

Respectfully submitted,



Robert E. Bushnell

Reg. No.: 27,774

1522 "K" Street, N.W., Suite 300
Washington, D.C. 20005
Area Code: (202) 408-9040

Folio: P56900
Date: 7 July 2003
I.D.: REB/kf

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SERIAL NUMBER

DOCKET NO. P56900

APPLICANT

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FILING DATE 7 July 2003

GROUP

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

TRANSLATION

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	YES	NO

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, etc.)*

[illegible]

EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP §609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.